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		Kavadias, et al., "A Logarithmic Response CMOS Image Sensor with On-Chip Calibration," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 35, No. 8, 2000.	
		Karim, et al., "On-Pixel Amorphous Silicon Amplifier for Digital Imaging in Integrated Optoelectronics," Deen, et al., editors; <i>The Electrochemical Society, Proc.</i> , Vol. PV2002-4, May 2002, pp. 313-332.	
		Karim, et al., "High Dynamic Range Pixel Architectures for Diagnostic Medical Imaging in Medical Imaging," <i>Physics of Medical Imaging, SPIE</i> , Vol. 5368, 2004, pp. 657-667.	
		Sanaie-Fard, et al., "High Dynamic Range Pixel Amplifier Architecture in Amorphous Silicon Technology for Diagnostic X-Ray Imaging Applications," <i>106th Meeting of the Electrochemical Society: Thin Film Transistor Technologies VIII (TF-TTVIII)</i> , PV 2004-15, pp. 289-300.	
		Taghibakhsh, et al., "High Dynamic Range 2-FT Amplified Pixel Sensor Architecture for Digital Mammography Tomosynthesis," <i>IET Circuits Devices & Systems</i> , Vol. 1, No. 1, 2007, pp. 87-92.	
		Taghibakhsh, et al., "Amplified Pixel Sensor Architecture for Low Dose Computed Tomography Using Silicon Thin Film Technology," <i>Proceedings of SPIE</i> , Vol. 6510, 2007.	

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